AMENDMENTS TO THE CLAIMS

(IN FORMAT COMPLIANT WITH THE REVISED 37 CFR 1.121)

Please cancel claims 2, 3, 4, 9, 13 and 14 without prejudice.

1. (CURRENTLY AMENDED) A method for circuit recovery from overstress conditions, comprising the steps of:

(A) detecting an event; and

(B) storing said event;

(C) comparing said stored event to a plurality of event types stored in a table to determine if said event is a first predetermined type or a second predetermined type; and

~~(B)~~ (D) resetting a device when said event is a said first predetermined type and providing recovery when said event is a said second predetermined type.

2. (CANCELLED)

3. (CANCELLED)

4. (CANCELLED)

5. (CURRENTLY AMENDED) The method according to claim 3 1, wherein step ~~(B)~~ (D) further comprises:

determining an appropriate recovery, wherein said recovery is selected from the group consisting of (i) self checking, (ii) issuing warnings, (iii) performing back-up operations, and (iv) shutting-down.

6. (ORIGINAL) The method according to claim 5, wherein said recovery further comprises resetting.

7. (CURRENTLY AMENDED) The method according to claim 1, wherein step ~~(B)~~ (D) further comprises:

performing recovery steps before or in place of a full reset.

8. (CURRENTLY AMENDED) The method according to claim 1, wherein step ~~(A)~~ (D) further comprises:

determining if resetting or providing recovery is necessary.

9. (CANCELLED)

10. (CURRENTLY AMENDED) The apparatus according to claim 1, wherein steps (A) and ~~(B)~~ (D) are performed by a processor.

11. (CURRENTLY AMENDED) An apparatus comprising:

means for detecting an event; and

means for storing said event;

means for comparing said event to a plurality of event types stored in a table to determine if said event is a first predetermined type or a second predetermined type; and

means for (i) resetting a device when said event is a said first predetermined type and (ii) providing recovery when said event is a said second predetermined type.

12. (CURRENTLY AMENDED) An apparatus comprising:

a detection circuit configured to generate a signal having an event condition; and

a storage circuit configured to store said event;

a table configured to store a plurality of event types; and

a circuit configured to (i) reset when said event condition is a first predetermined type and (ii) implement recover action when said event condition is a second predetermined type, wherein said first and second predetermined types are determined in response to a comparison of said event to said plurality of event types stored in said table.

13. (CANCELLED)

14. (CANCELLED)

15. (CURRENTLY AMENDED) The apparatus according to claim 14, wherein said circuit is further configured to determine an appropriate recovery action.

16. (ORIGINAL) The apparatus according to claim 12, wherein said circuit comprises a microprocessor.

17. (ORIGINAL) The apparatus according to claim 12, wherein said detection circuit comprises an over/under-voltage detection circuit.

18. (ORIGINAL) The apparatus according to claim 12, wherein said detection circuit comprises a high current detection circuit.

19. (ORIGINAL) The apparatus according to claim 12, wherein said detection circuit comprises a noise coupling detection circuit.

20. (ORIGINAL) The apparatus according to claim 12, wherein said detection circuit comprises:

an over/under-voltage detection circuit;

a high current detection circuit; and

a noise coupling detection circuit.